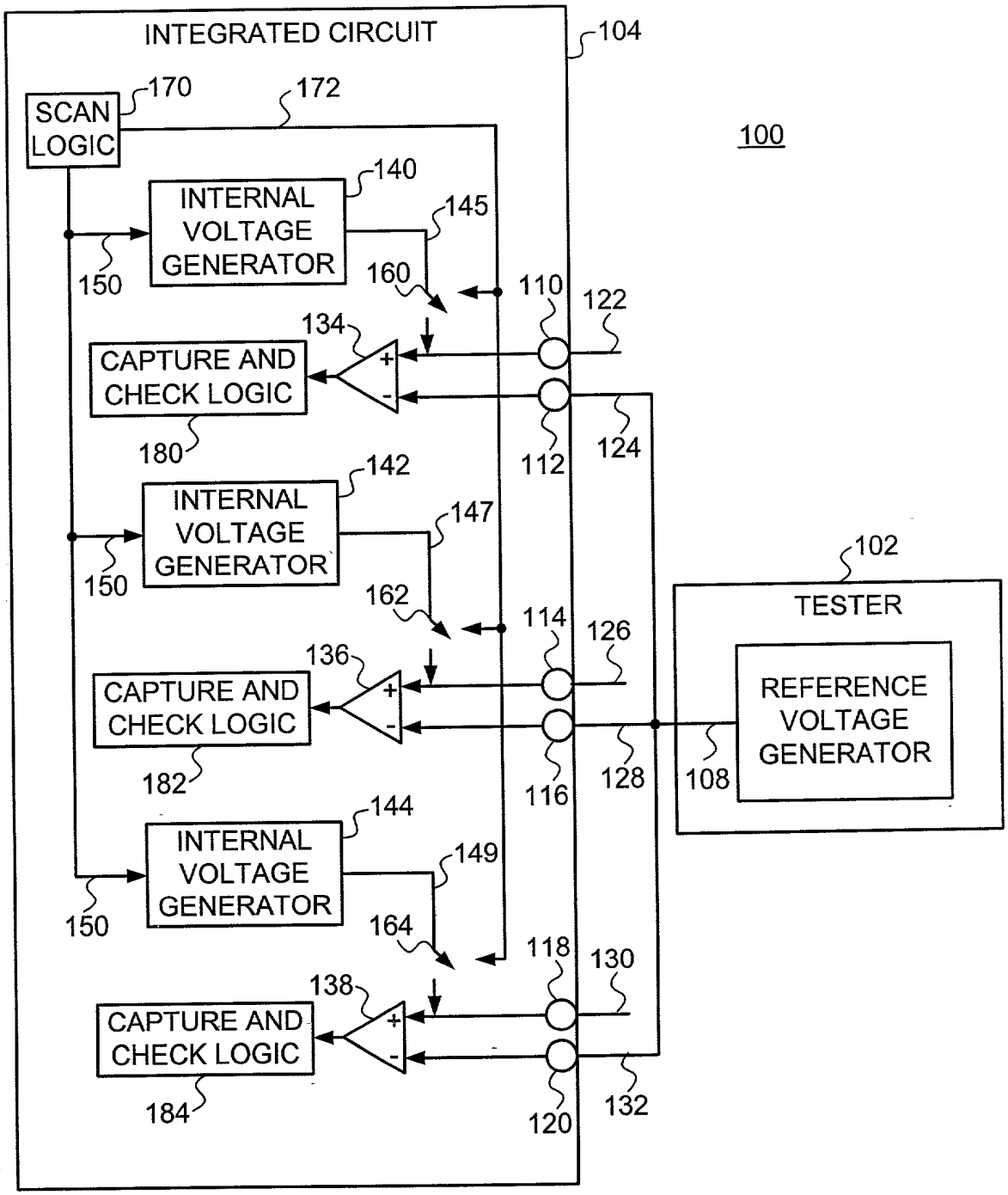
INTEGRATED CIRCUIT
104
100
SCAN LOGIC
170
172
INTERNAL VOLTAGE GENERATOR
140
145
150
160
110
122
134
CAPTURE AND CHECK LOGIC
180
112
124
INTERNAL VOLTAGE GENERATOR
142
147
150
162
114
126
136
CAPTURE AND CHECK LOGIC
182
116
128
102
TESTER
REFERENCE VOLTAGE GENERATOR
108
INTERNAL VOLTAGE GENERATOR
144
149
150
164
118
130
138
CAPTURE AND CHECK LOGIC
184
120
132

FIGURE 1

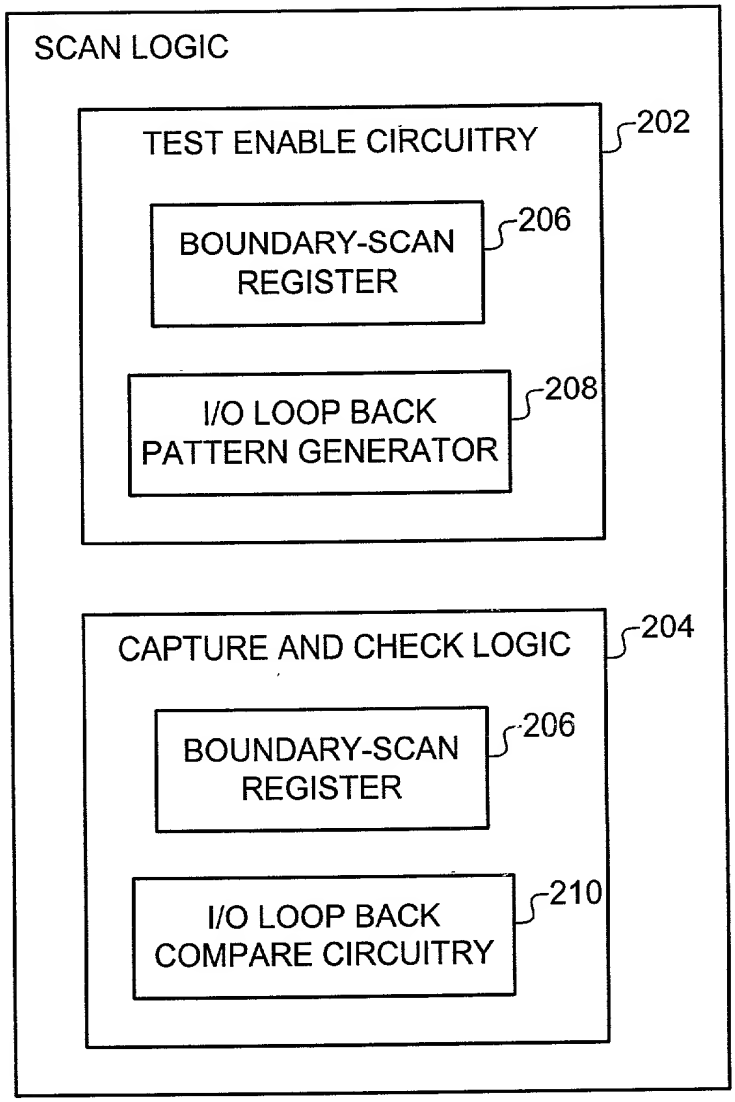
170
SCAN LOGIC
TEST ENABLE CIRCUITRY
202
BOUNDARY-SCAN REGISTER
206
I/O LOOP BACK PATTERN GENERATOR
208
CAPTURE AND CHECK LOGIC
204
BOUNDARY-SCAN REGISTER
206
I/O LOOP BACK COMPARE CIRCUITRY
210

FIGURE 2

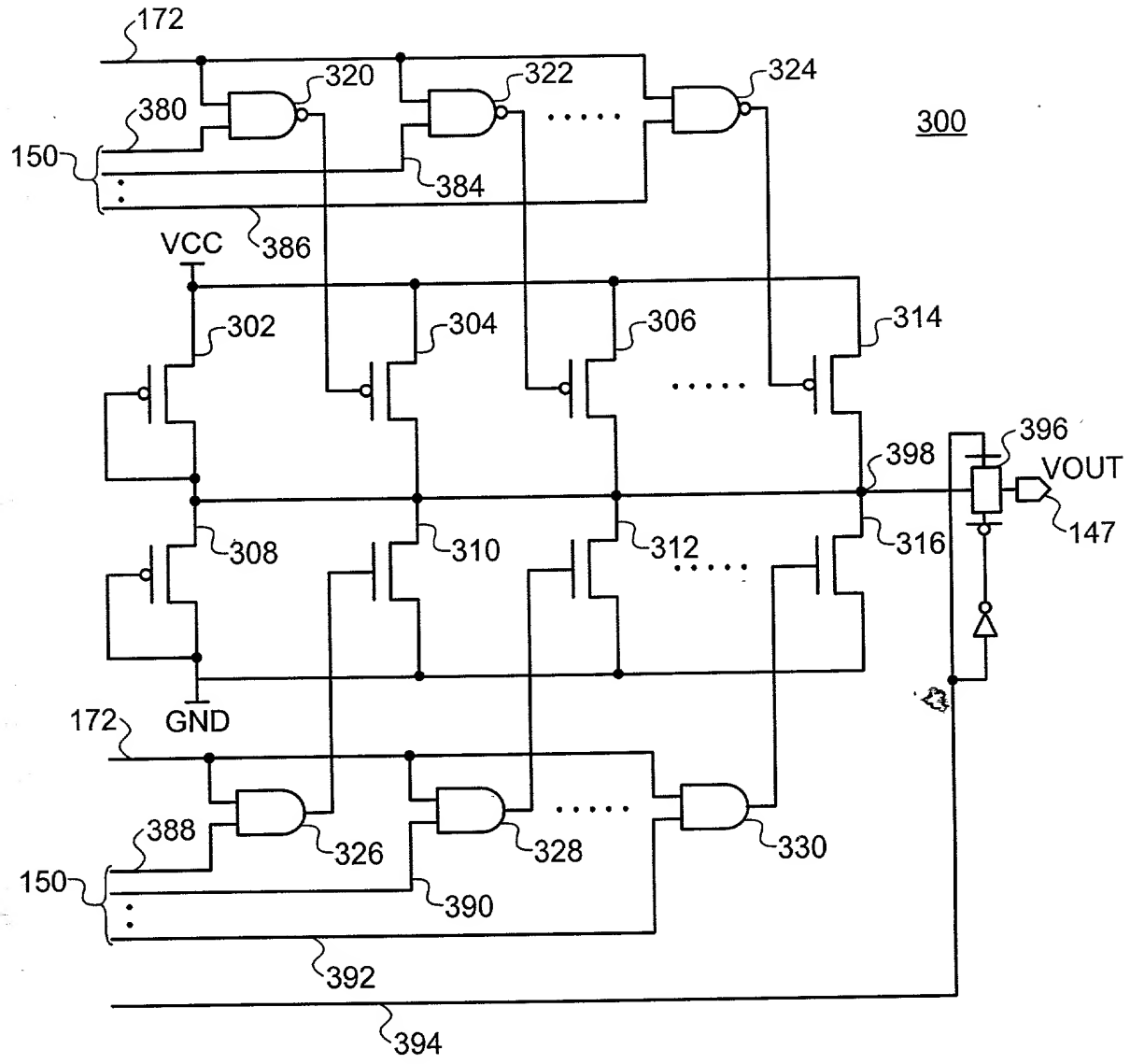
172
320
322
324
380
300
150
384
VCC
386
302
304
306
314
396
398
VOUT
308
310
312
316
147
GND
172
388
326
328
330
150
390
392
394

FIGURE 3

400

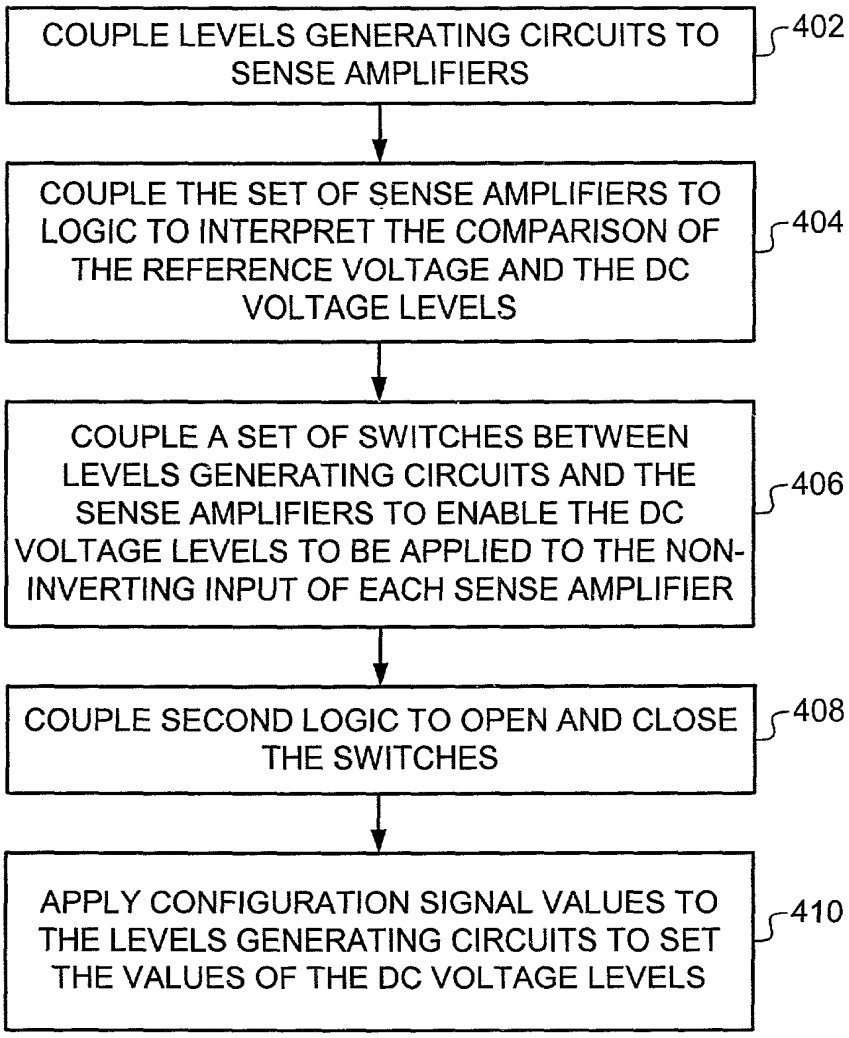

FIGURE 4

Docket No. 4-4-02